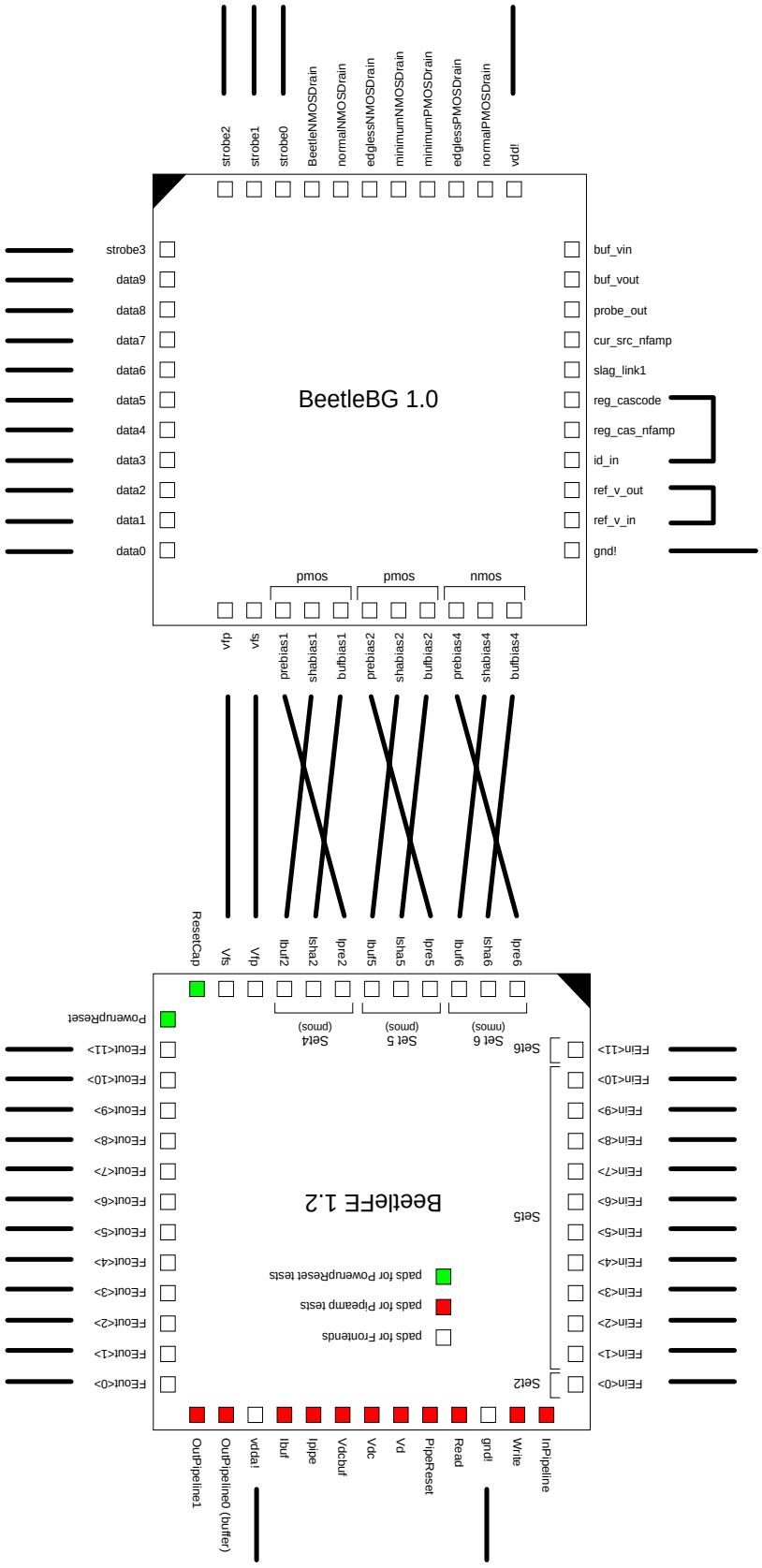
BeetleBG 1.0
vdd!
normalPMOSDrain
edglessPMOSDrain
minimumPMOSDrain
minimumNMOSDrain
edglessNMOSDrain
normalNMOSDrain
BeetleNMOSDrain
strobe0
strobe1
strobe2
gnd!
ref_v_in
ref_v_out
id_in
reg_cas_nfamp
reg_cascode
slag_link1
cur_src_nfamp
probe_out
buf_vout
buf_vin
nmos
pmos
pmos
bufbias4
shabias4
prebias4
bufbias2
shabias2
prebias2
bufbias1
shabias1
prebias1
vfs
vfp
data0
data1
data2
data3
data4
data5
data6
data7
data8
data9
strobe3
InPipeline
Write
gnd!
Read
PipeReset
Vd
Vdc
Vdcbuf
Ipipe
Ibuf
vdda!
OutPipeline0 (buffer)
OutPipeline1
Ipre6
Isha6
Ibuf6
Ipre5
Isha5
Ibuf5
Ipre2
Isha2
Ibuf2
Vfp
Vfs
ResetCap
FEin<11>
FEin<10>
FEin<9>
FEin<8>
FEin<7>
FEin<6>
FEin<5>
FEin<4>
FEin<3>
FEin<2>
FEin<1>
FEin<0>
Set6
Set5
Set2
Set6 (nmos)
Set 5 (pmos)
Set4 (pmos)
BeetleFE 1.2
pads for Frontends
pads for Pipeamp tests
pads for PowerupReset tests
FEout<0>
FEout<1>
FEout<2>
FEout<3>
FEout<4>
FEout<5>
FEout<6>
FEout<7>
FEout<8>
FEout<9>
FEout<10>
FEout<11>
PowerupReset