



**Testpulse
Generator**

I_{tp}
 V_{fp}
 V_{fs}
 I_{pre}
 I_{sha}
 I_{buf}
 I_{comp}
 V_{rc}
 I_{thmain}
 $I_{thdelta}$
**Frontend
Bias-Generator**

**Pipeline and Readout
Control**

**I2C
Interface**

V_{dcl}
 V_d
 $I_{voltbuf}$
 I_{pipe}
 I_{sf}
 I_{curbuf}
**Backend
Bias-Generator**