

notCompOut<8>	CompClk	gnd	
CompOut<8>	notCompClk	(2.5V) vdd	
notCompOut<9>			
CompOut<9>			
notCompOut<10>			Datavalid
CompOut<10>		CompOut<0>	notDatavalid
notCompOut<11>	CompClk	notCompOut<0>	TP
CompOut<11>	notCompClk	CompOut<1>	notTP
RoReTokenOut	CompOut<8>	notCompOut<1>	Trigger
RoTokenIn	notCompOut<8>	CompOut<2>	notTrigger
FifoFull	CompOut<9>	notCompOut<2>	WriteMon
notCompOut<15>	notCompOut<9>	CompOut<3>	TrigMon
CompOut<15>	CompOut<10>	notCompOut<3>	
notCompOut<14>	notCompOut<10>	CompOut<4>	CompOut<12>
CompOut<14>	CompOut<11>	notCompOut<4>	notCompOut<12>
notCompOut<13>	notCompOut<11>	CompOut<5>	CompOut<13>
CompOut<13>	ProbeloutBE	notCompOut<5>	notCompOut<13>
notCompOut<12>		CompOut<6>	CompOut<14>
CompOut<12>		notCompOut<6>	notCompOut<14>
		CompOut<7>	CompOut<15>
TrigMon		notCompOut<7>	notCompOut<15>
WriteMon		RoTokenOut	FifoFull
notTrigger		RoReTokenIn	
Trigger			
notClock			
Clock			
notTP			
TP			
notReset			
Reset			
notDatavalid			
Datavalid			
SCL			
SDA			

red = no. 2 (bottom)
blue = no. 1 (top)
black = both